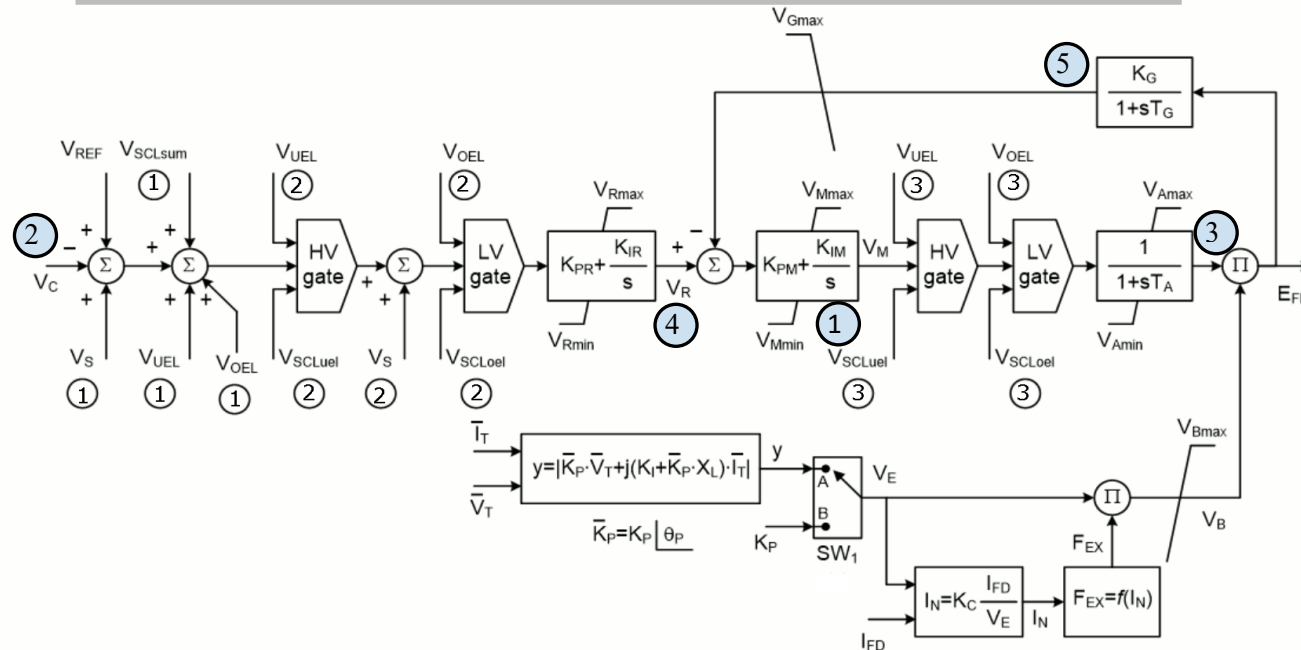


Exciter ST4C

Exciter ST4C IEEE Std 421.5-2016 Static exciter with potential, compound, or independent source-controlled



States:

- 1 – V_M
- 2 – Sensed V_T
- 3 – V_A
- 4 – V_R
- 5 – V_G

alternate
PSS input
locations
(V_S)

- ① summation point before take-over UEL
- ② summation point after take-over UEL

alternate
SCL input
locations
(V_{SCL})

- ① summation point (voltage error)
- ② take-over at voltage regulator input
- ③ take-over at inner-loop output

alternate
OEL input
locations
(V_{OEL})

- ① summation point (voltage error)
- ② take-over at voltage regulator input
- ③ take-over at inner-loop output

alternate
UEL input
locations
(V_{UEL})

- ① summation point (voltage error)
- ② take-over at voltage regulator input
- ③ take-over at inner-loop output

AC Rotating Exciter

AC Rotating Exciter
AC rotating exciter with non-controlled rectifier model

States:

$1 - V_E$

